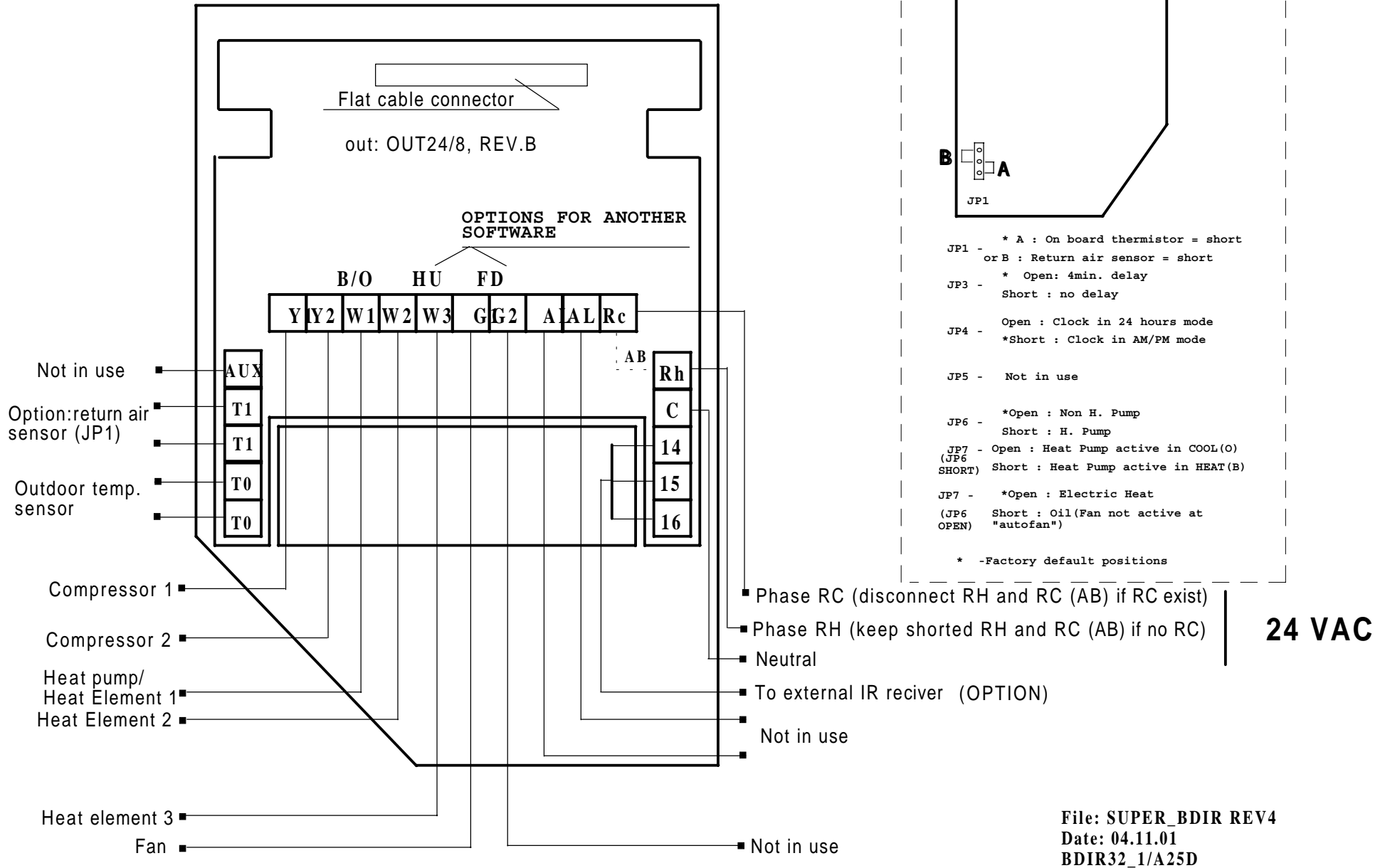
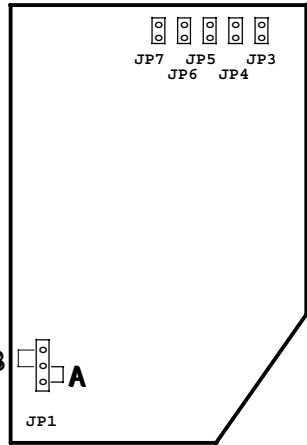


SUPER BCIR

ELECTRICAL DIAGRAM



JUMPERS DIAGRAM



- JP1 - * A : On board thermistor = short
or B : Return air sensor = short
 - JP3 - * Open: 4min. delay
Short : no delay
 - JP4 - Open : Clock in 24 hours mode
*Short : Clock in AM/PM mode
 - JP5 - Not in use
 - JP6 - *Open : Non H. Pump
Short : H. Pump
 - JP7 - Open : Heat Pump active in COOL(O)
(JP6 SHORT) Short : Heat Pump active in HEAT(B)
 - JP7 - *Open : Electric Heat
(JP6 SHORT) Short : Oil (Fan not active at "autofan")
- * -Factory default positions

- Phase RC (disconnect RH and RC (AB) if RC exist)
- Phase RH (keep shorted RH and RC (AB) if no RC)
- Neutral
- To external IR reciver (OPTION)
- Not in use
- Not in use

24 VAC

File: SUPER_BDIR REV4
Date: 04.11.01
BDIR32_1/A25D